

FIG. 3

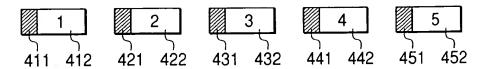


FIG. 4

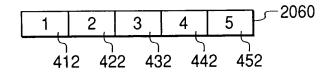


FIG. 5

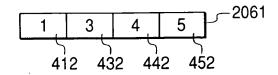


FIG. 6

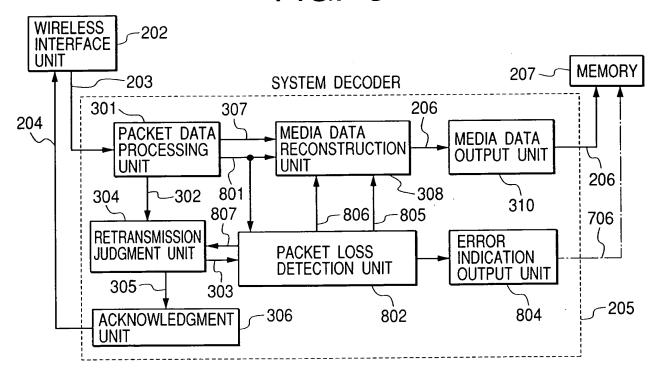
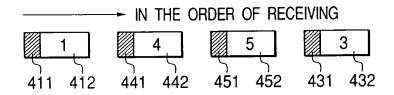


FIG. 7(a)



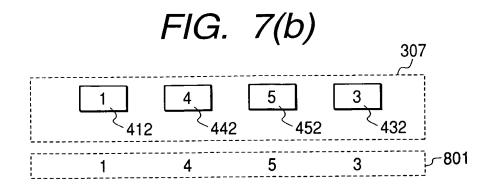
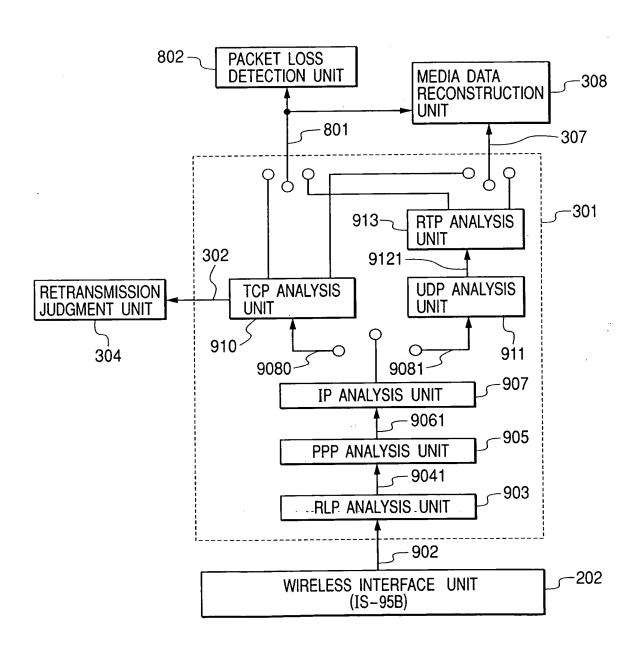


FIG. 8





IPv4 PACKET

VERSION (4 BITS)	HEAD (4 BI	ER LENGTH		RIORIT BITS		SERVIC (5 BITS	E TYPE		L IP LENGTH (TES)
DATAGRAM (2 BYTES)		FRAGMENT (2 BYTES)	Γ			LIVE BYTE)	PROTO (1 BYT		CHECKSUM (2 BYTES)
SOURCE PORT DESTINATION F ADDRESS (A BYTES) (4 BYTES)			PORT	PAYLOAD (VARIABLE LENGTH)			LENGTH)		
9061	7								908

FIG. 10

9041 PPP FRAME **DESTINATION CYCLIC** PAYLOAD (VARIABLE **REDUNDANCY PORT PROTOCOL FLAG** CONTROL **FLAG** CHECK (1 BYTE) **ADDRESS** (2 BYTES) (1 BYTE) (1 BYTE) LENGTH) (2 BYTES) (1 BYTE) 906

	902			
RLP FRAME				
SEQUENCE NUMBER (1 BYTE)	TYPE OF FRAME (1 BIT)	PAYLOAD LENGTH (7 BITS)	PAYLOAD (VARIABLE)	PADDING (VARIABLE)
			904	

FIG. 12

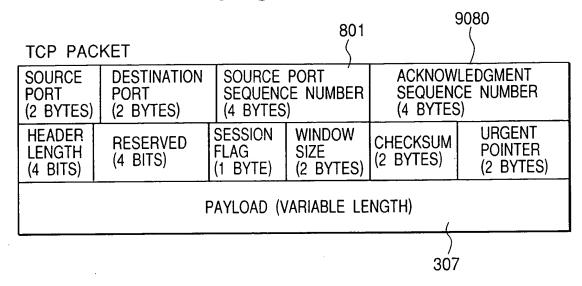
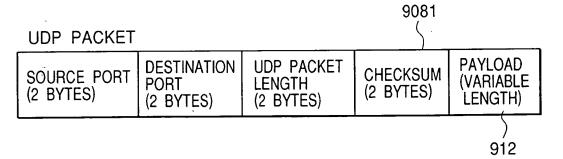


FIG. 13



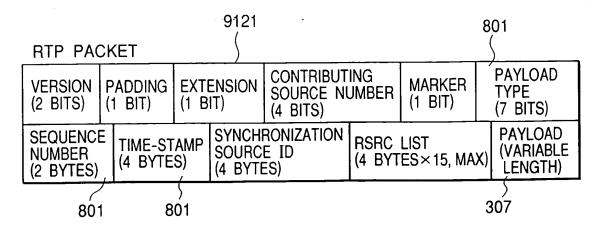
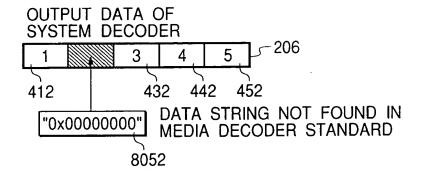


FIG. 15



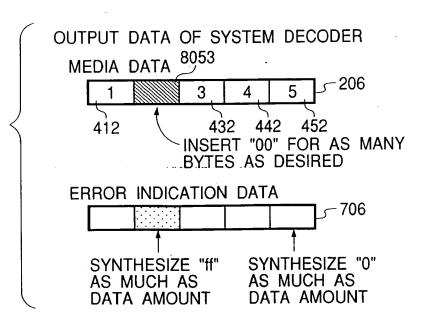


FIG. 17

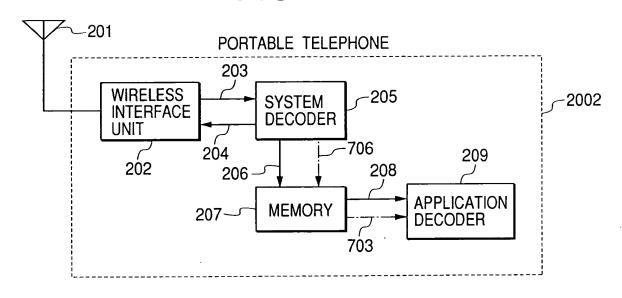
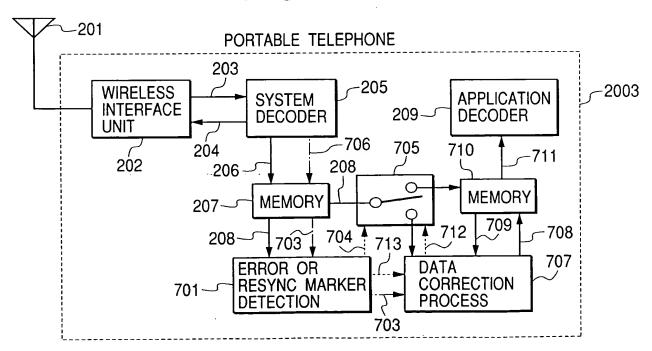


FIG. 18



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FIG. 19

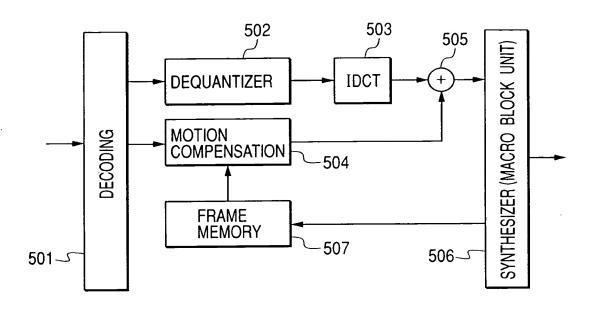


FIG. 20

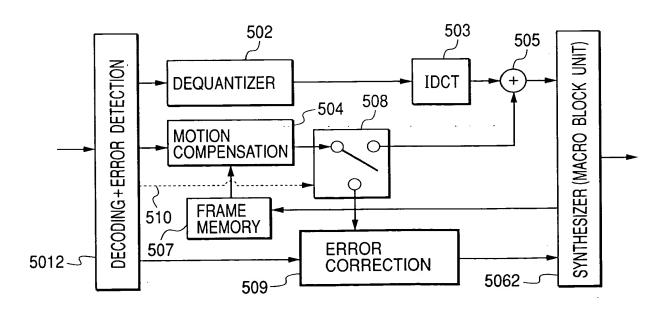


FIG. 21

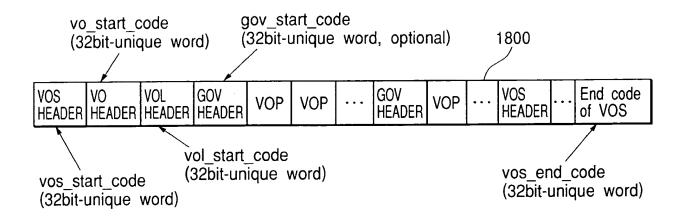
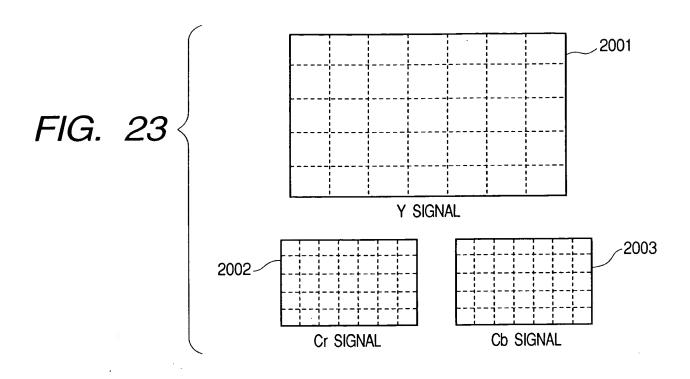


FIG. 22

1900

modulo time base marker bit vop coding type vop start code (1bit AND ABOVE, TERMINATE WITH "0") (2bit) (1bit) (32bit-unique word) vop rounding_type vop_time_increment marker bit -vop coded (1-16bit VARIABLE) (IN CASE OF 1bit, vop coding type !="1") (1bit) (1bit) vop fcode forward (IN CASE vop fcode backward(IN CASE intra de vle thr vop_quant OF 3bit, vop coding type !="1") OF 3bit, vop coding type=="B") (5bit) (3bit)

11/21



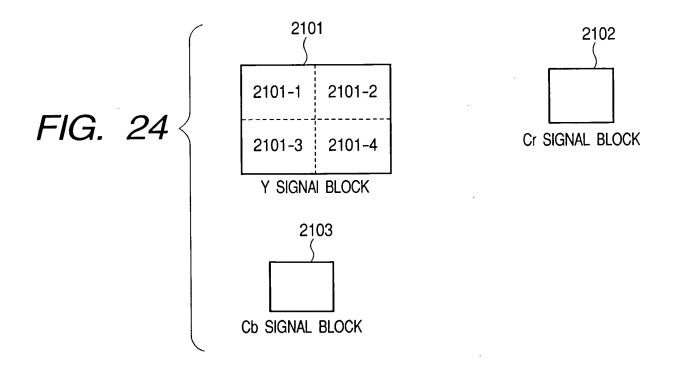


FIG. 25

2200

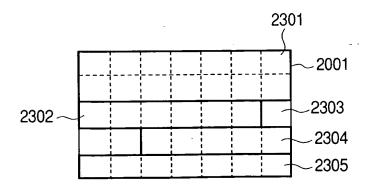
not_coded (1bit, vop_codir _type==IN CASE OF "p"	ng mcbpc (1-9bit VAF	RIABLE)	ac_pred_flag (1bit, mb_type== IN CASE OF intra or intra+q)	
cpby (1-6bit, mb_type != IN CASE OF stuffing)	dquant (2bit, m ==IN CASE (intra+q or inte	OF	MOTION VECTOR (mb_type ==inter, inter+q or inter4v)	
DIFFERENTIAL intra DC (mb_type==intra or intra- _intra_dc_vlc==IN CASE	⊦q AND use	Intra AC COEFFICIENT or inter DC & AC COEFFICIENT (BLOCK DESIGNATED BY cbpy, cbpci)		

mcbpc: mb_type (intra, intra+q, inter, inter+q, inter4v, stuffing), cbpc not_coded: IN CASE OF "1", mb_type=inter, NO MOTION, mcbpc

AND THEREAFTER OMITTED

use <code>intra_dc_vlc</code> : <code>DETERMINE BY quant AND intra_dc_vlc_thr</code>, <code>AND TAKE THE VALUE OF "0" OR "1"</code>

FIG. 26



VIDEO PACKET DATA (I-VOP)

11000 1110121 21111 (2 10)									
VIDEO PACKET HEADER	PRIORITY DATA PART (I-VOP)	dc_marker (19bit)	AC COEFFICIENT CONTROL INFORMATION (ac_pred_flag, cbpy)	AC COEFFICIENT INFORMATION					
2401	2402	2403	2404	2405					

FIG. 28

quant scale

(5bit)

macroblock number

(1-14bit)

header_extension_code (1bit) marker_bit vop_coding_type (1bit) (2bit)

2401

modulo_time_b (1bit_AND_ABC TERMINATE_WIT	ase VE, H "0")	marker_bit (1bit)	vop_time_incre (1-16bit_VARI/	ement ABLE)
intra_dc_vlc_thr (3bit)	(1	op_fcode_forv N CASE OF oding_type !=	3bit, vop_	vop_ (IN type

vop fcode backward (IN CASE OF 3bit, vop coding type == "B")

FIG. 29

24021

PRIORITY DATA PART (I-VOP)

resync marker

(17-23bit-unique word)

modulo_time_base

mcbpc: mb_type (intra, intra+q stuffing), cbpc

use_intra_dc_vlc: DETERMINE BY quant AND intra_dc_vlc_thr,

AND TAKE THE VALUE OF "0" OR "1"

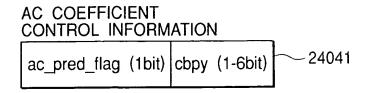


FIG. 31

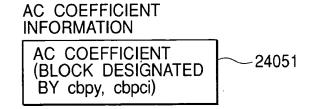


FIG. 32

VIDEO PACKET DATA (P-VOP)

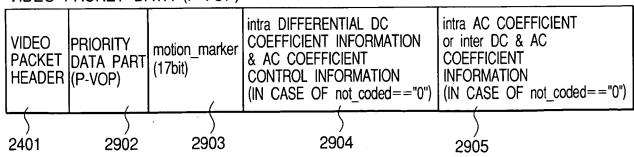


FIG. 33

PRIORITY DATA PART (P-VOP)

not_coded (1bit) | mcbpc (1-9bit VARIABLE)

MOTION VECTOR (mb type==inter, inter+q, or inter4v)

29021

mcbpc: mb_type (intra, intra+q, inter, inter+q, inter4v, stuffing), cbpc IN CASE OF not_coded: "1", mb_type=inter, NO MOTION, mcbpc AND THEREAFTER OMITTED

FIG. 34

intra DIFFERENTIAL DC COEFFICIENT INFORMATION & AC COEFFICIENT CONTROL INFORMATION

29041

ac_pred_flag (IN CASE OF
1bit, mb_type==intra or
intra+q)

cbpy (1-6bit)

dquant (IN CASE OF 2bit, mb_type==intra+q or inter+q)

DIFFERENTIAL intra DC COEFFICIENT (mb_type==intra or intra+q)

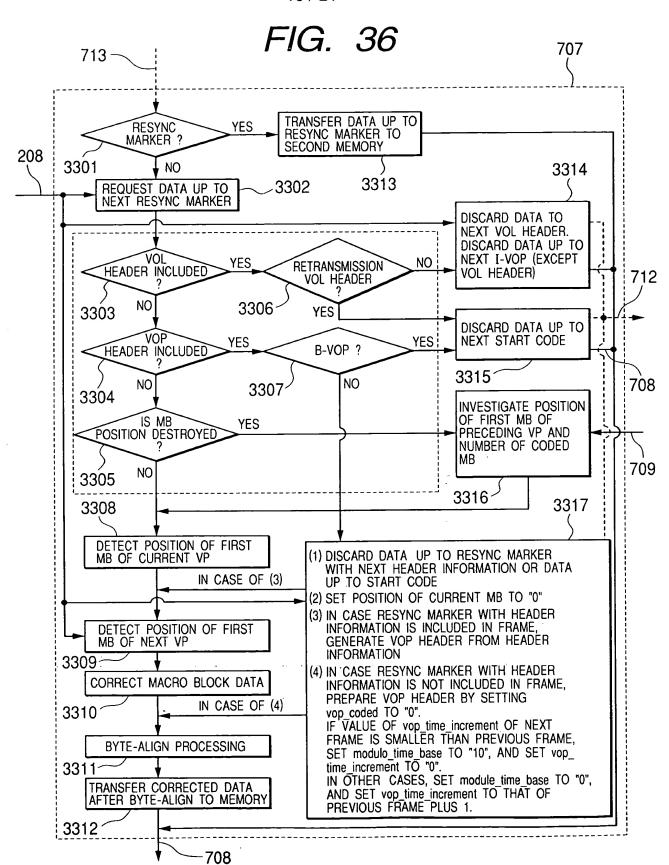
FIG. 35

intra AC COEFFICIENT or inter DC & AC COEFFICIENT INFORMATION

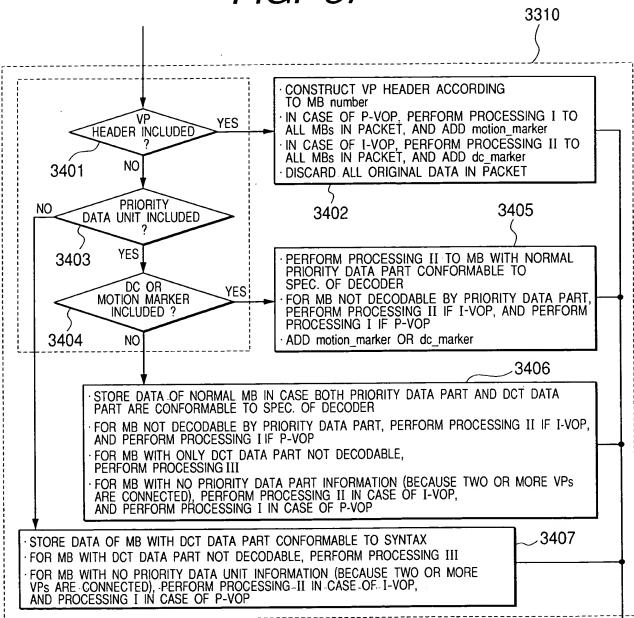
intra AC COEFFICIENT or inter DC & AC COEFFICIENT (BLOCK DESIGNATED BY cbpy, cbpci

29051

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PROCESSING I: SET not coded FLAG TO "1". DELETE ORIGINAL DATA IN MB.

PROCESSING II: SET ALL DIFFERENTIAL DC COEFFICIENTS IN MB TO "0" AND SET mb_type TO "intra" AND SET cpby AND cbpc (mcbpc) TO NO CODED BLOCK. DELETE ORIGINAL DATA IN MB.

PROCESSING III: SET cpby AND cbpc (mcbpc) TO NO CODED BLOCK.

FURTHER, IN CASE OF I-VOP, SET ac_pred_flag TO "0", AND DELETE AC COEFFICIENT DATA. IN CASE OF P-VOP, PERFORM PROCESSING I IF INTRA CODING. IF mb_type

IS PREDICTIVE CODING DELETE inter DC & AC COEFFICIENT DATA.

FIG. 38

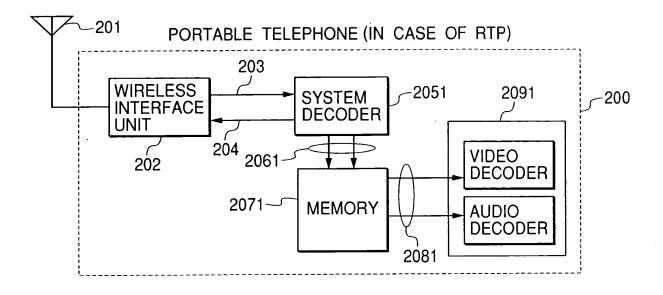


FIG. 39

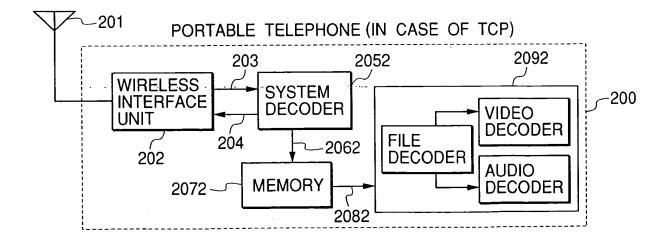


FIG. 40

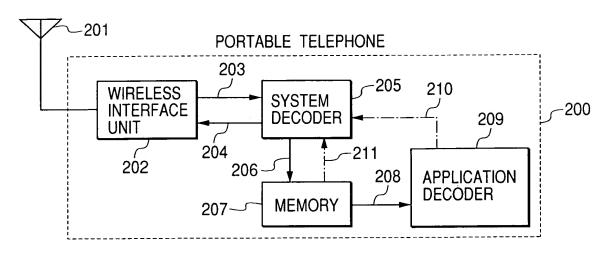


FIG. 41

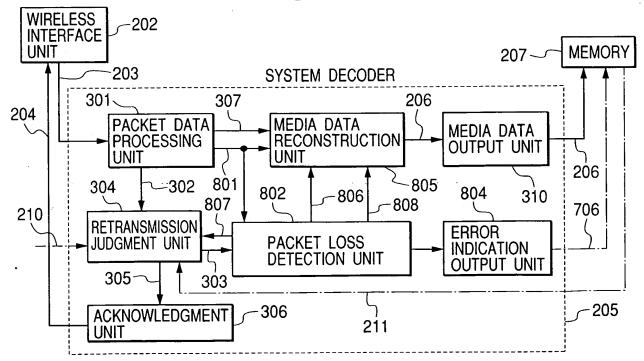


FIG. 42

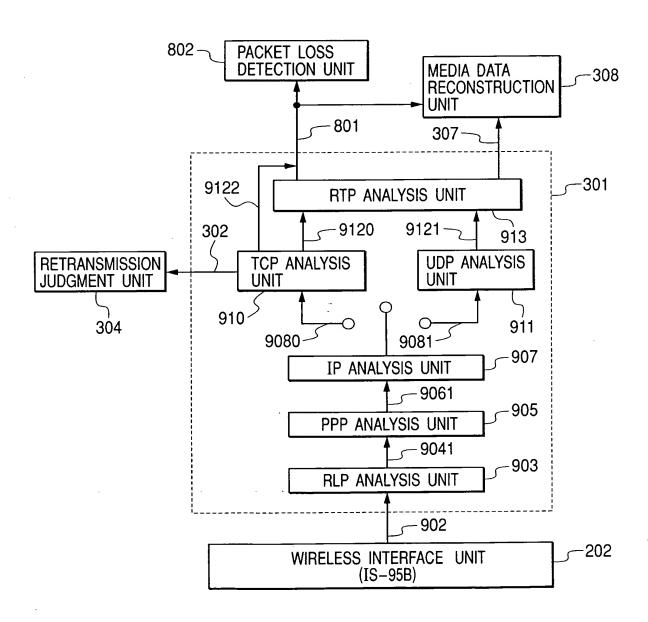


FIG. 43

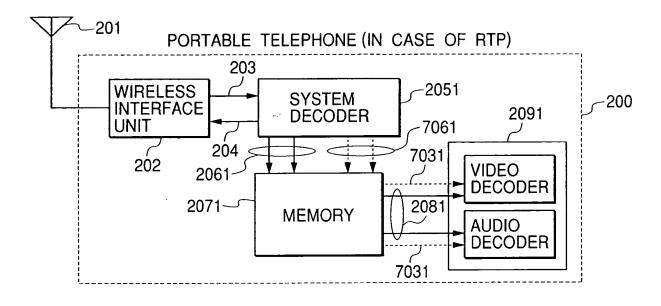


FIG. 44

